Figure 1
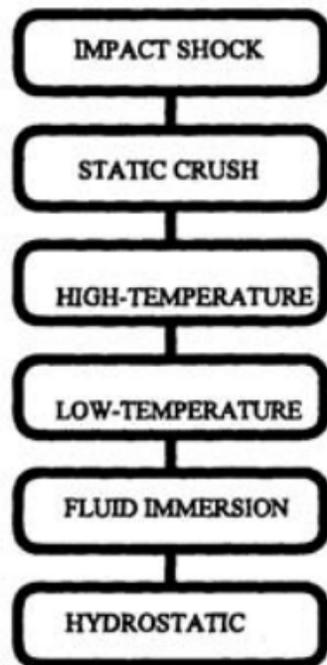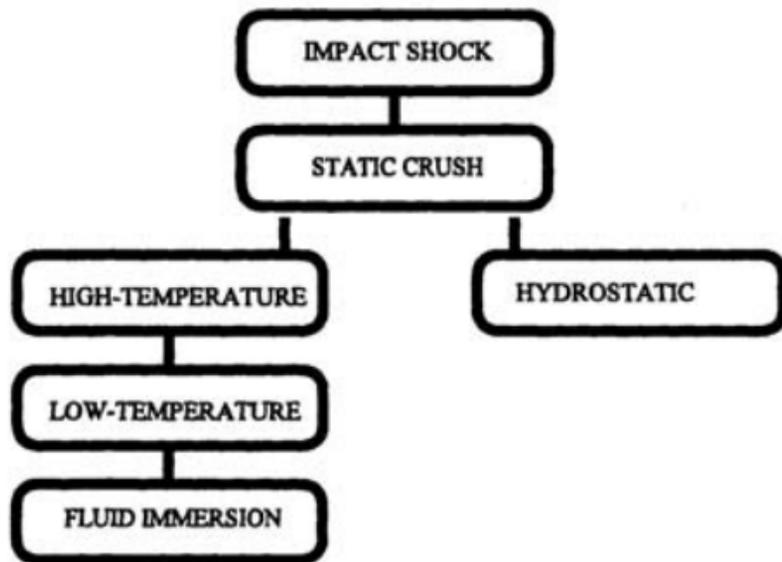

EVENT RECORDER MEMORY MODULE
SEQUENTIAL TESTING

SINGLE BRANCH SEQUENTIAL TESTING